



MataiTech launches NAUET 1.7 including the world's first SPIRIT IP-XACT Linter

MataiTech, December 12, 2006

URL: <http://www.pr9.net/science/electronics/4890december.html>

MataiTech LLC has released the latest version of its flagship EDA tool, NAUET 1.7 (newt). It includes the world's first SPIRIT IP-XACT linter and a royalty free, light weight RTOS.

PR9.NET December 12, 2006 - Rancho Santa Margarita, California – MataiTech LLC has announced the latest release of its flagship EDA product, NAUET 1.7 (newt). MataiTech has enhanced its co-development tool, NAUET (newt), by including the world's first full featured SPIRIT IP-XACT editor, linter and semantics checker. NAUET validates and lints all aspects of the SPIRIT IP-XACT standard and checks for consistency across multiple files. NAUET 1.7 (newt) is capable of converting existing SPIRIT IP-XACT 1.2 designs into the forthcoming SPIRIT IP-XACT 1.4 standard. NAUET 1.7 is part of MataiTech's commitment to track and lead standards to the benefit of its customers.

"NAUET is a must for anyone trying to make their components IP-XACT compliant." claimed Aaron Baranoff, Vice President of Engineering at MataiTech, "With the new Component Editor any IP vendor or user can create or edit components and verify that they meet the IP-XACT standards. With NAUET's 1.7 Component Editor it is nearly impossible to create a non-SPIRIT compliant IP block without being notified and flagged by the tool."

NAUET 1.7 now includes a light weight RTOS framework generator. This provides a low overhead, royalty free operating system for SoC development and testing. NAUET customizes and builds a framework for each component in the SoC. This light weight RTOS framework is designed to simplify product development and make product development less expensive.

"With the inclusion of the RTOS framework customers can opt out of large footprint RTOS's and use our simple and fast micro scheduler based RTOS. This is all part of MataiTech's commitment to accelerate our customer's design flow while reducing their risk," explained Rob Fleck, Principal Marketing Manager at MataiTech.

NAUET(newt) is MataiTech's 4th generation co-development tool. Compatible with major EDA tools, NAUET creates a parallel, synchronous design flow environment for co-development of SoC and software. It automatically links IP blocks, generates Verilog, VHDL, C, SystemC, RTOS framework, design documentation and test code. NAUET accelerates HDL coding and C/C++ coding to quickly allow engineers to begin application, driver and HDL development. Designs created with NAUET Component Editor are reusable as IP in later projects. NAUET is bundled with many OpenCores IP blocks so customers can quickly create an SoC right out of the box.

NAUET 1.7 provides Windows Vista 32 (x86) and 64 bit support. A scaled down "Lite" version and a Student/Teacher version will be launched coincidentally with full featured version.

NAUET's IP-XACT 1.4 (IP-XACT with ESL Extensions) features will be made available to SPIRIT members only. All of the other powerful NAUET 1.7 features will be available to any and all users.

NAUET Lite is intended for IP providers and for customers not building large SoCs. It is useful for editing IP blocks or assembling small SoCs. NAUET Lite is equipped with all the powerful features available in the full featured version. It is limited only by the number of IP blocks that can be connected in one design.

NAUET Student/Teacher is targeted for Non-Commercial use as an educational tool and to help give real experience to students, teachers and hobbyists who want to build SoCs. This version is limited in terms of size of components as well as number of IP Blocks in a SoC.

Free licenses for 30 day trials are available to interested customers and press.

###

About MataiTech

Located in Orange County California, MataiTech can bring designs to market faster than anybody in the industry. Our core engineering team has been using hardware/software co-simulation technology for years to help develop networking, encryption, parallel processing, multi-million gate ASICs, FPGAs and embedded operating systems for all industries.

Phone:	949-226-7079
Website:	http://www.mataitech.com
E-Mail:	rob@mataitech.com
Address:	6 Santa Nella Rancho Santa Margarita CA 92688